

Substitute for form 1449A&B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>			Complete if Known		
			<i>Application Number / Confirmation Number</i>		
			10/660,449 / 4907		
			<i>Filing Date</i>		
			09/11/2003		
			<i>First Named Inventor</i>		
Warren E. Cory					
<i>Art Unit</i>					
2182					
<i>Examiner Name</i>					
Tanh Q. Nguyen					
<i>Sheet</i>	1	of	2	<i>Attorney Docket Number</i>	X-1214 US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Kind Code ^(if known)			
		US-6,995,618	2/7/2006	Boecker	
		US-6,908,340	6/21/2005	Shafer	
		US-6,812,870	11/2/2004	Kryzak et al.	
		US-6,975,132	12/13/2005	Groen et al.	
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		US-7,109,809	9/19/2006	Groen et al.	
		US-7,280,590	10/9/2007	Boecker et al.	
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		US-7,313,176	12/25/2007	Groen	
		US-7,047,457	5/16/2006	Black et al.	
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		US-7,188,283	3/6/2007	Shafer et al.	
		US-7,382,823	6/3/2008	Cory	
		US-7,187,709	3/6/2007	Menon et al.	
		US-7,099,426	8/29/2006	Cory et al.	
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials *	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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			<i>Art Unit</i>	2182	
			<i>Examiner Name</i>	Tanh Q. Nguyen	
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		Number Kind Code ^(if known)			
		10/659,803 filed 9/11/2003, BLACK, WILLIAM C., et al., Analog Front-End Having Built-In Equalization and Applications Thereof, Xilinx, Inc. 2100 Logic Drive, San Jose, California 95124			
		10/660,254 filed 9/11/2003, GROEN, ERIC D., et al., MGT/FPGA Clock Management System, Xilinx, Inc. 2100 Logic Drive, San Jose, California 95124			
		10/661,016 filed, 9/11/2003, GROEN, ERIC D., et al., Programmable Logic Device Including Programmable Multi-Gigabit Transceivers, Xilinx, Inc. 2100 Logic Drive, San Jose, California 95124			

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